

1/22

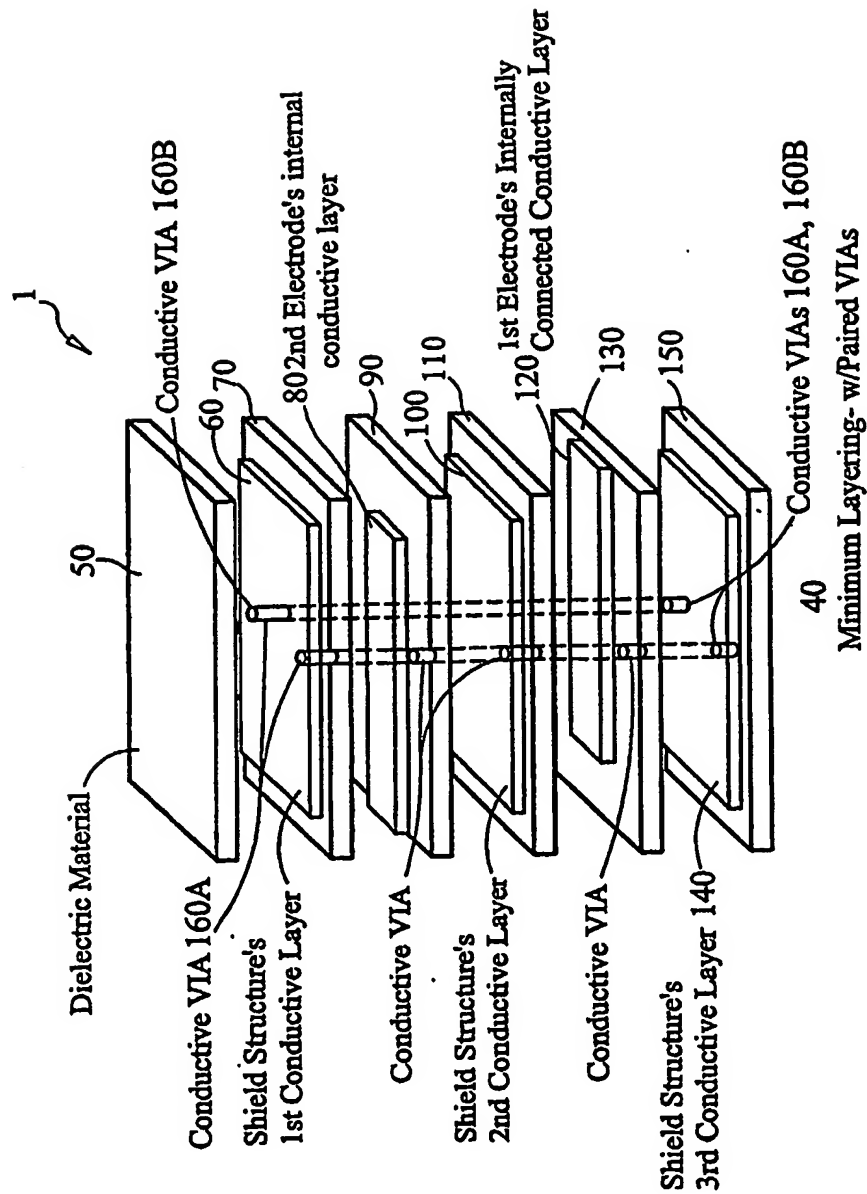


FIG. 1A

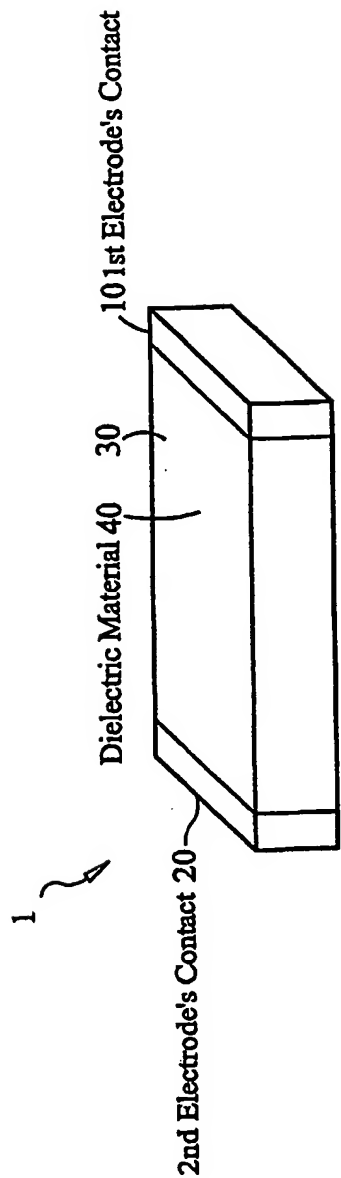


FIG. 1B

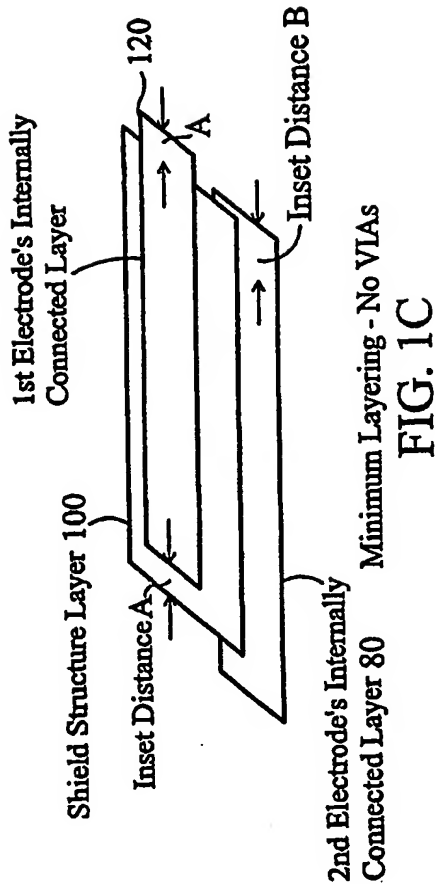


FIG. 1C

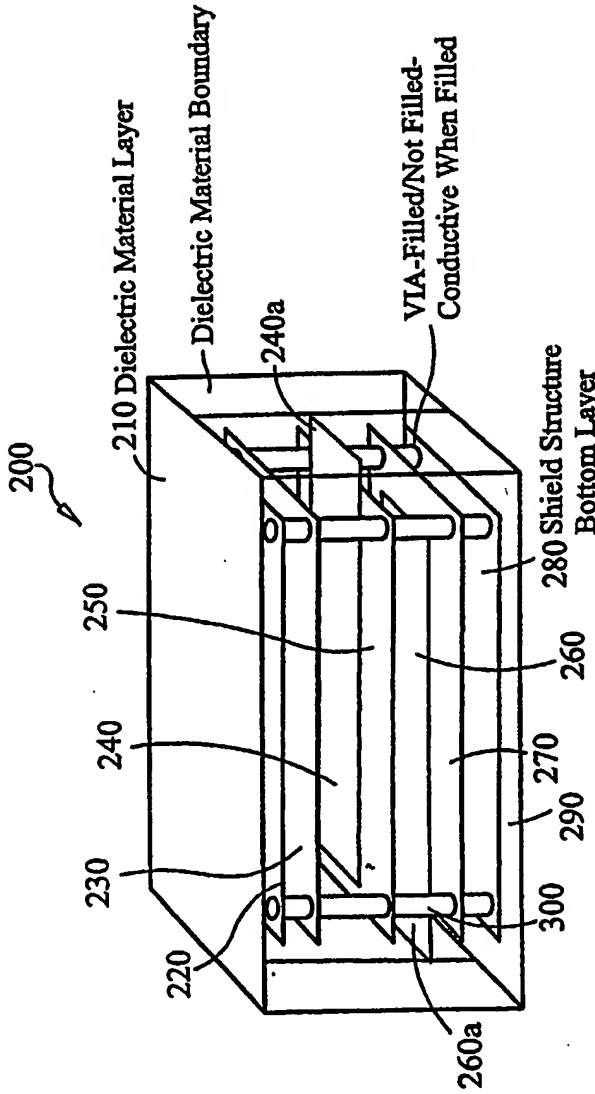


FIG. 2A

4/22

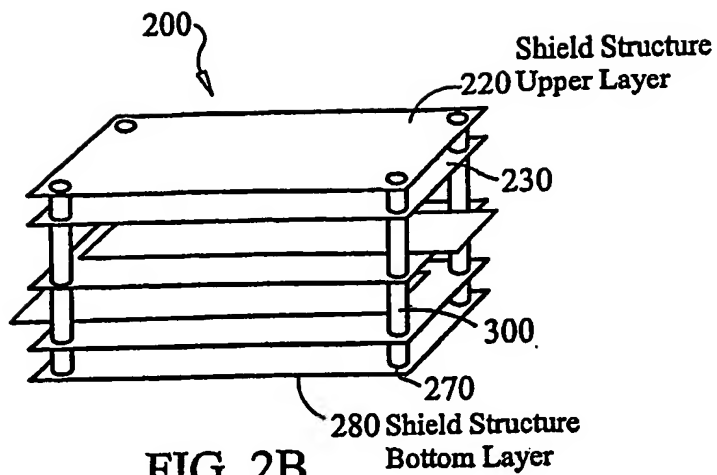


FIG. 2B

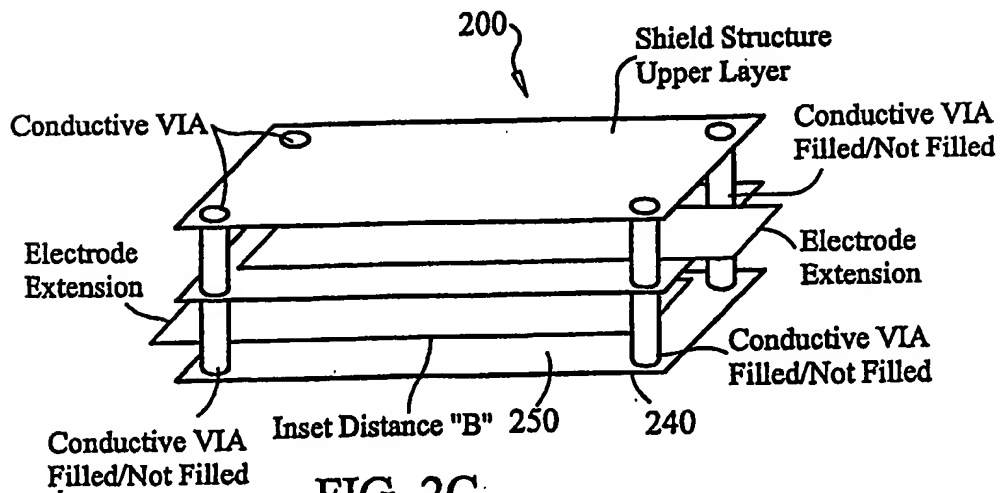
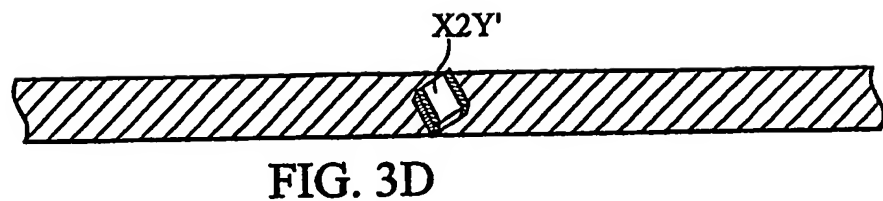
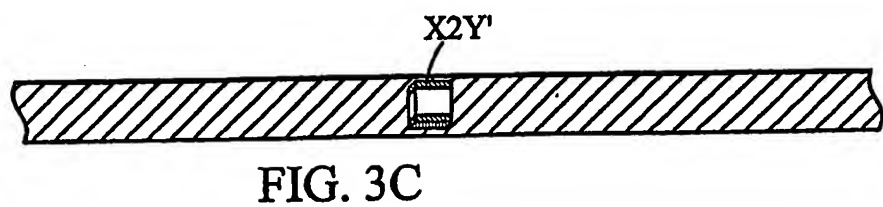
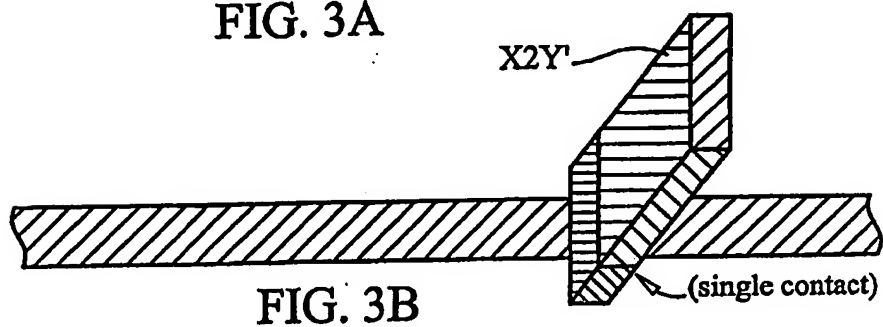
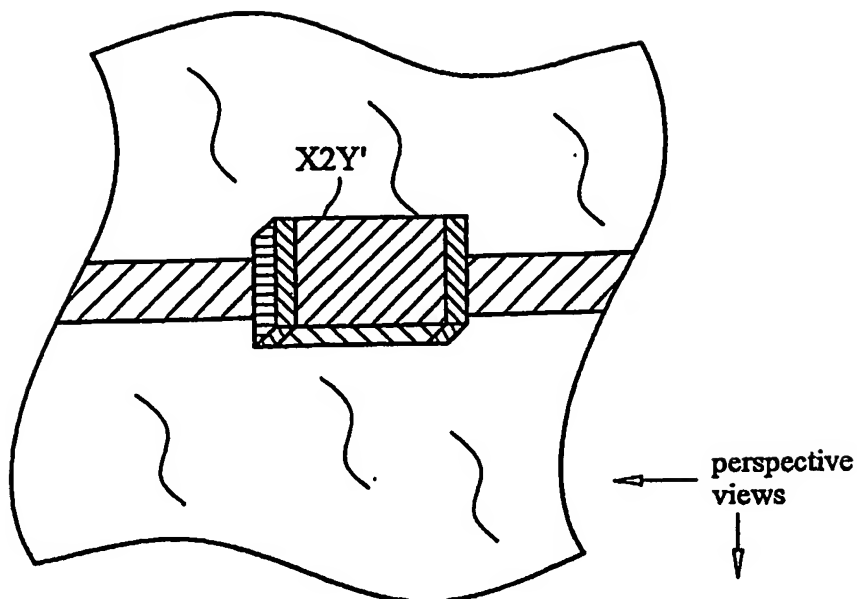
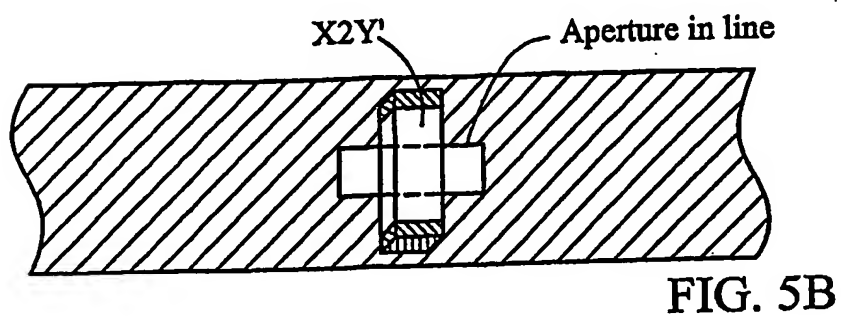
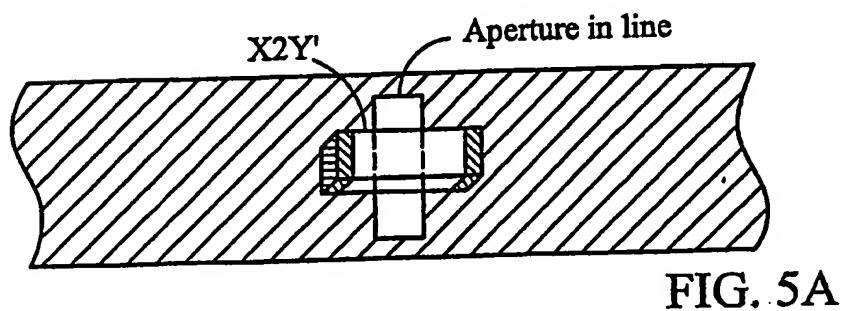
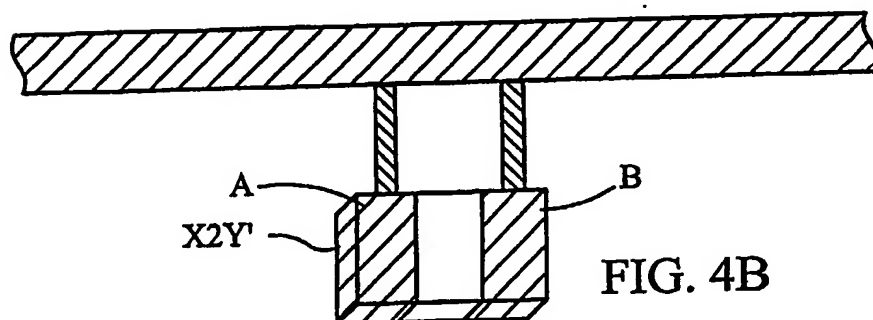
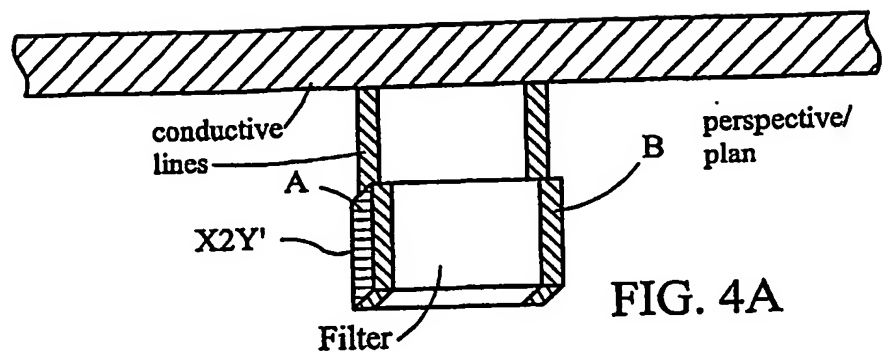


FIG. 2C

5/22



6/22



7/22

Filter Arrangement

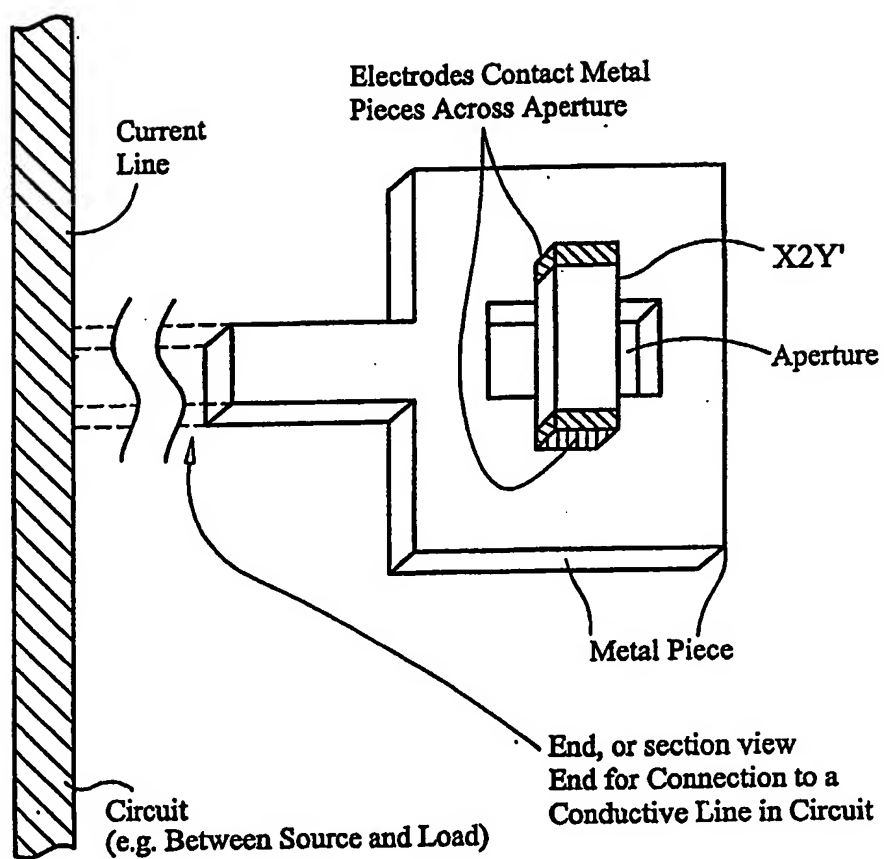


FIG. 6

8/22

Alternative Filter Arrangement

Plan View

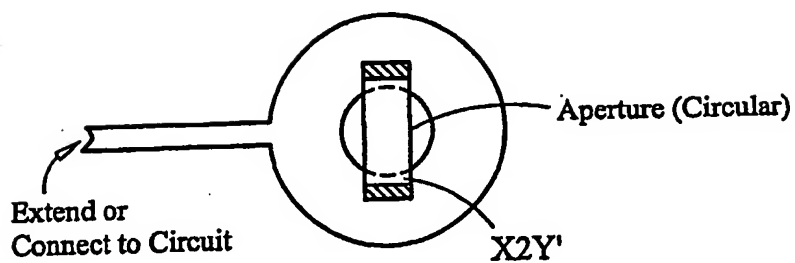


FIG. 7

Plan View

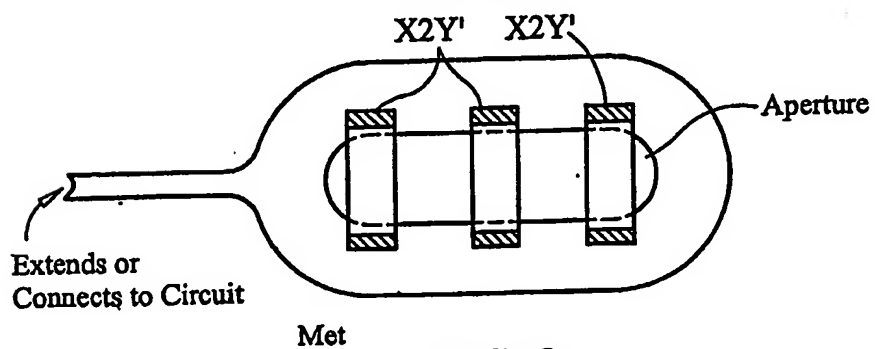
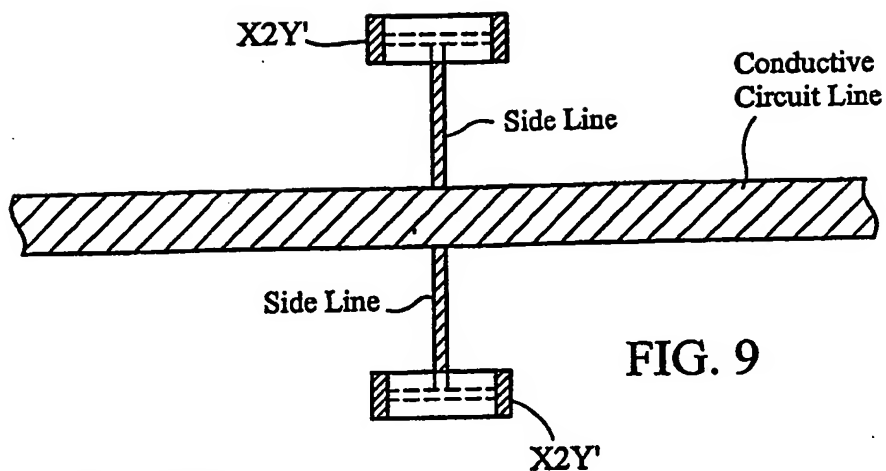


FIG. 8

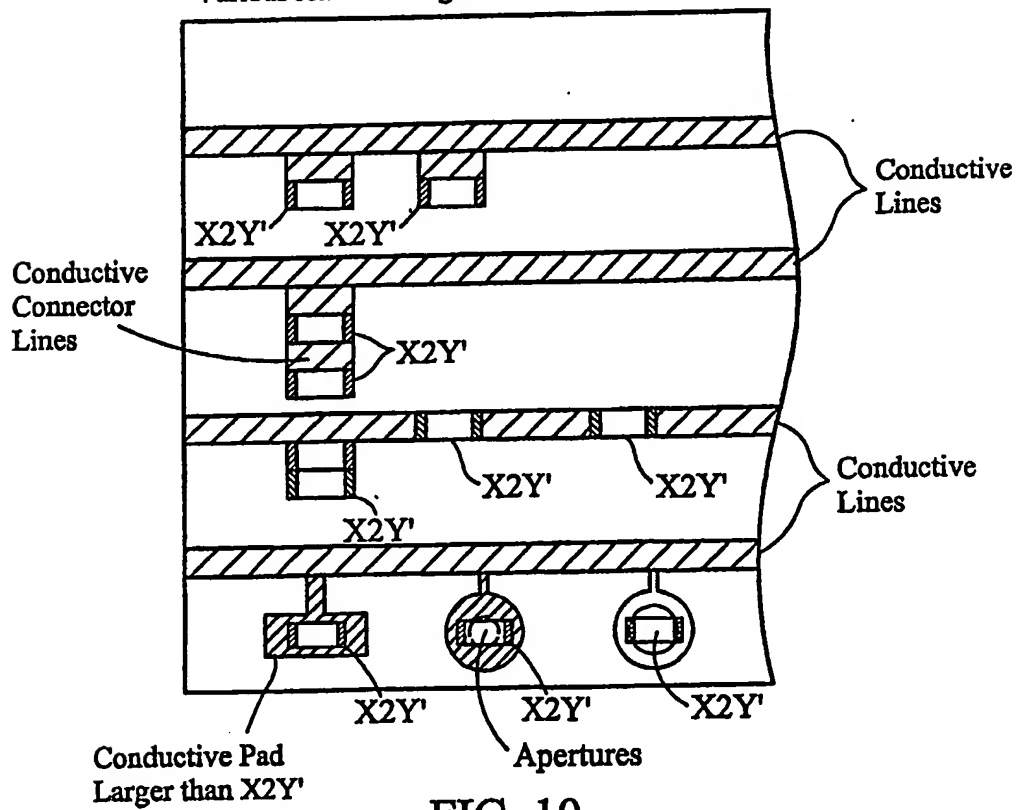
9/22



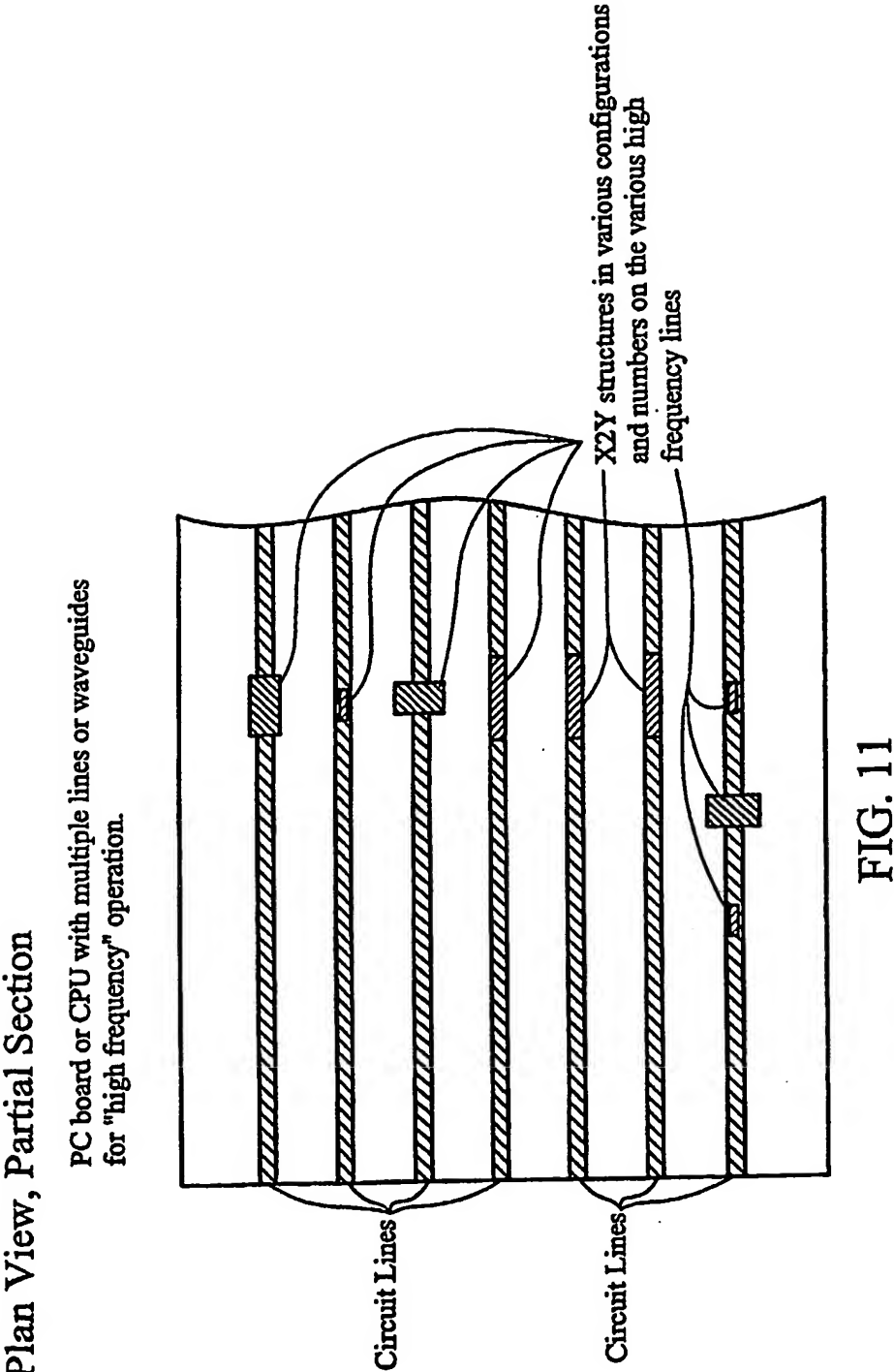
Plan View

PC or IC

Various X2Y' Arrangements



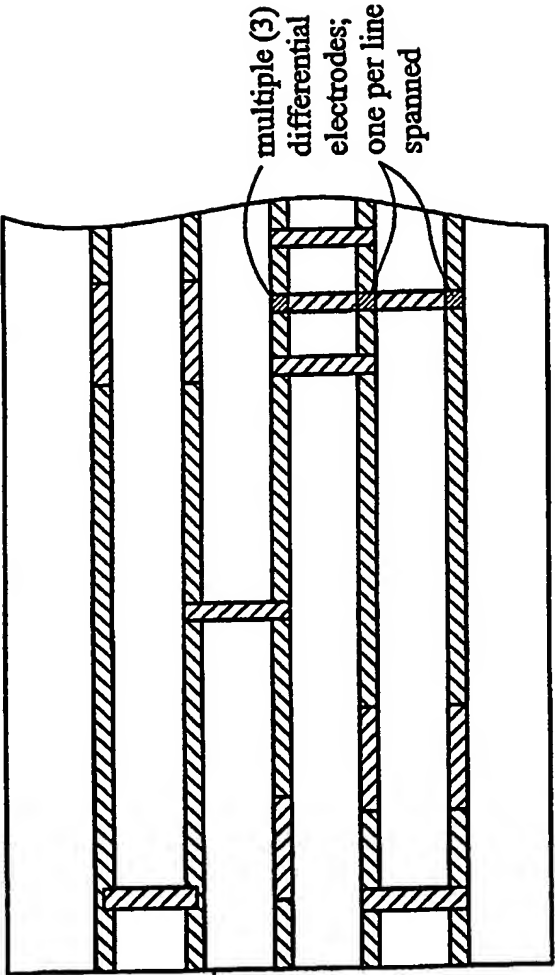
10/22



11/22

Plan View, Partial Section

PC board or CPU with multiple lines or waveguides
for "high frequency" operation.



X2Y' structure in various configurations
and number; some spanning at least 2 lines.
Preferably, one differential electrode per
line spanned.

FIG. 12

12/22

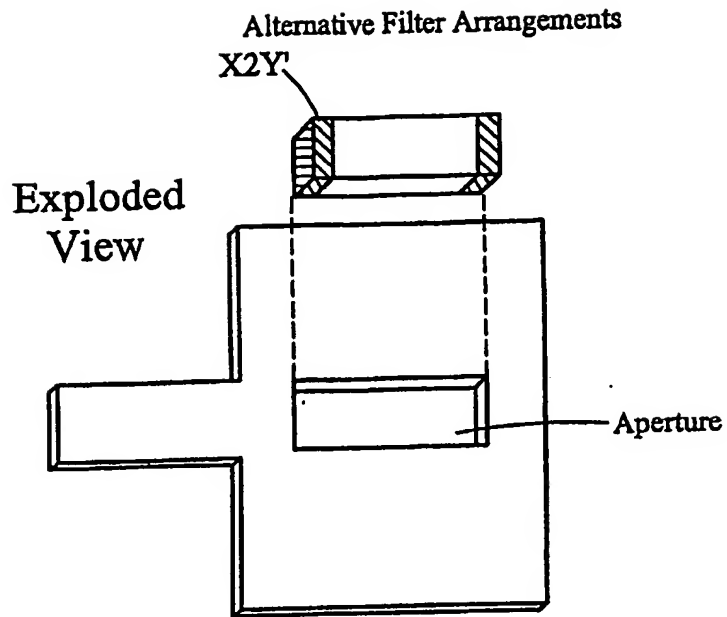


FIG. 13A

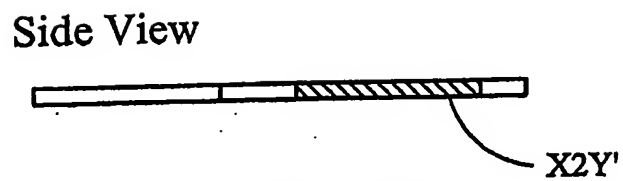


FIG. 13B

13/22

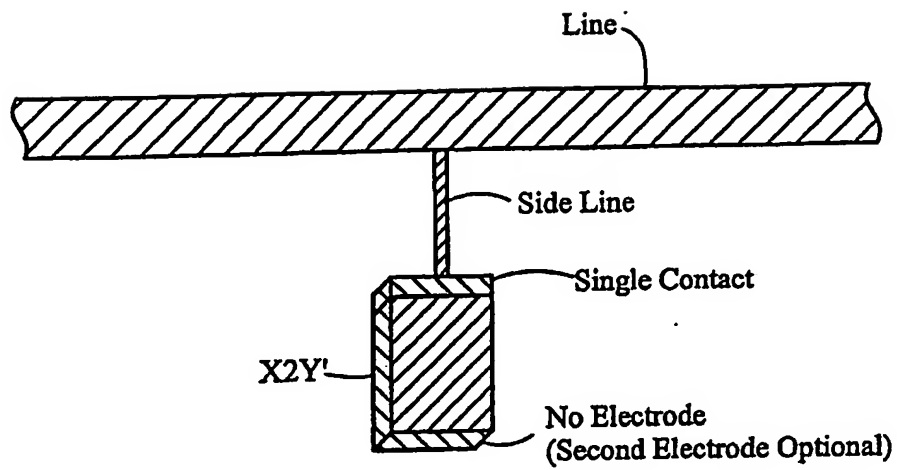


FIG. 14

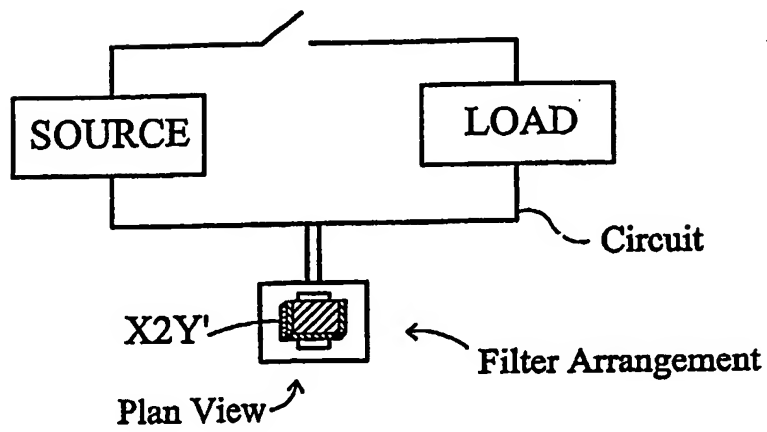


FIG. 15

14/22

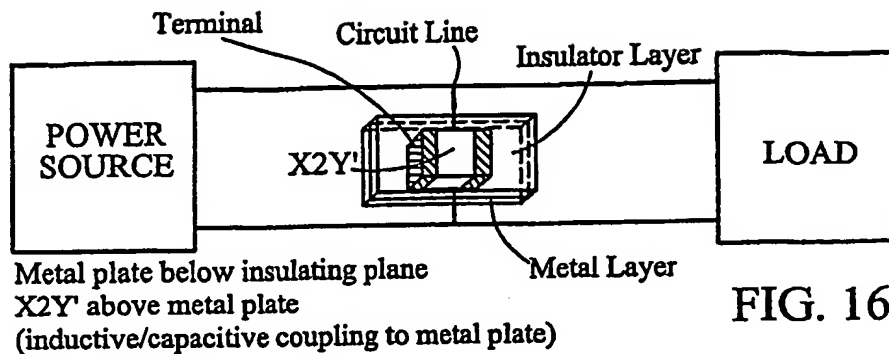


FIG. 16

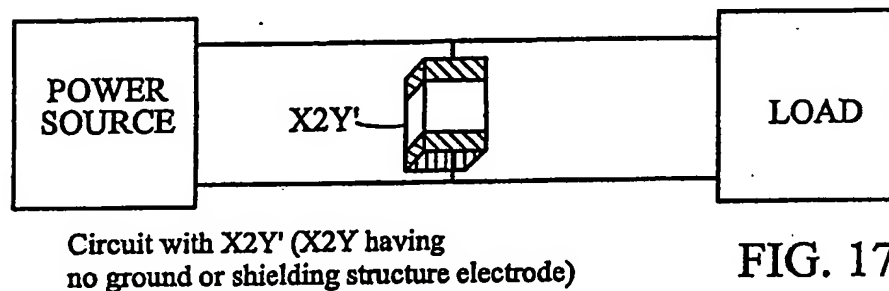


FIG. 17

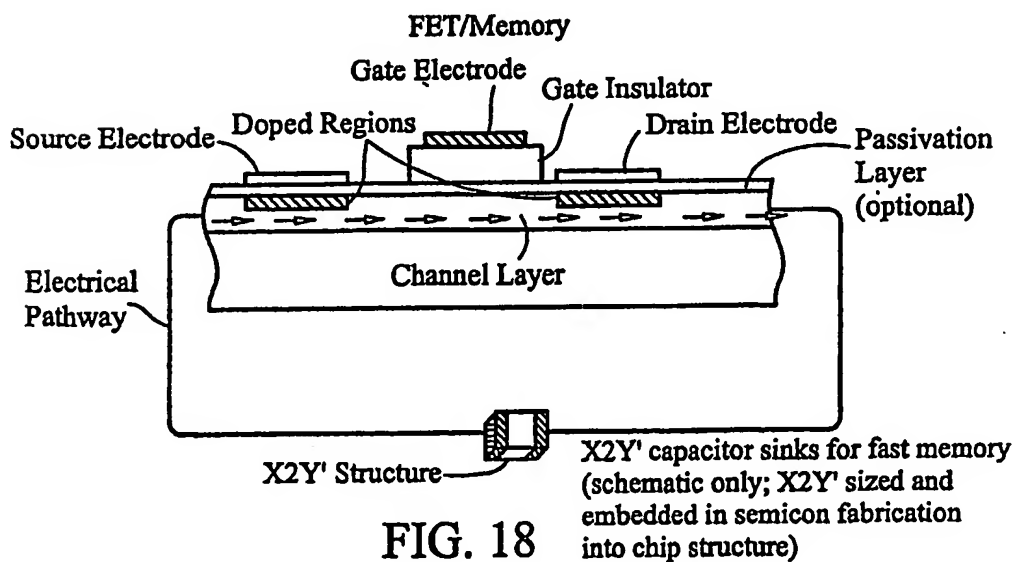


FIG. 18

15/22

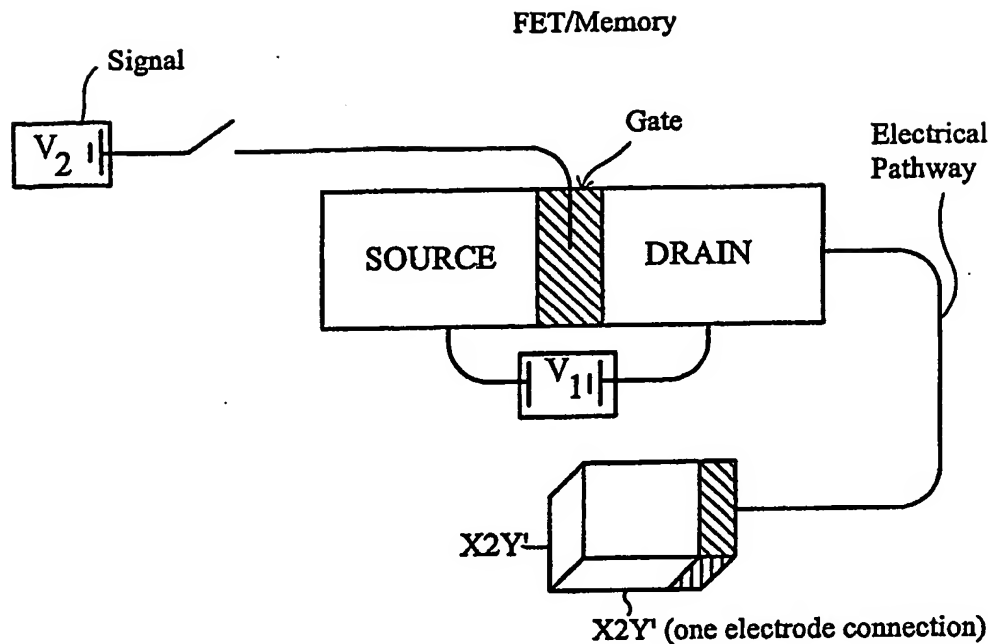


FIG. 19A

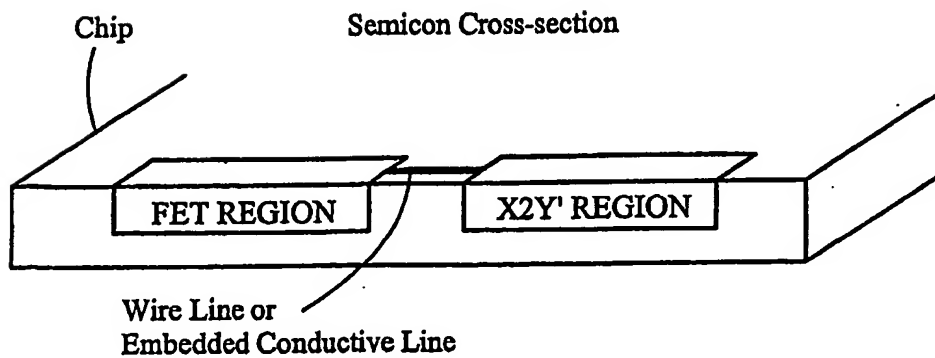


FIG. 19B

16/22

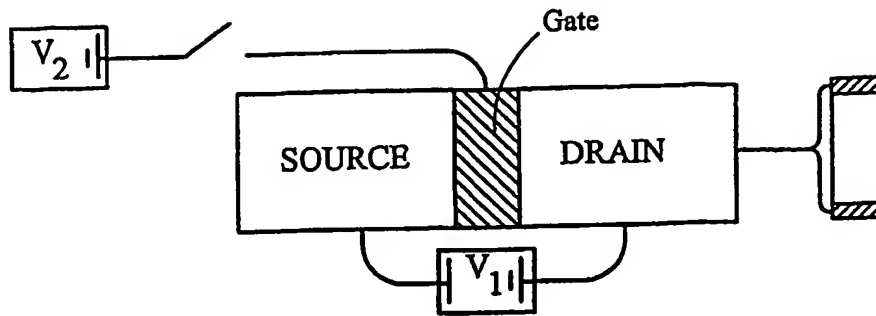


FIG. 20A

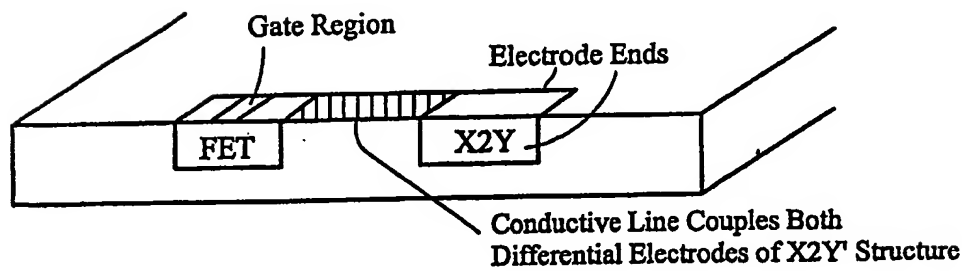


FIG. 20B

17/22

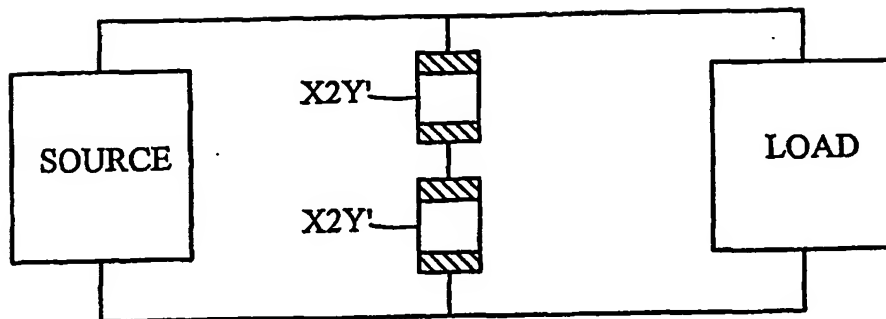


FIG. 21

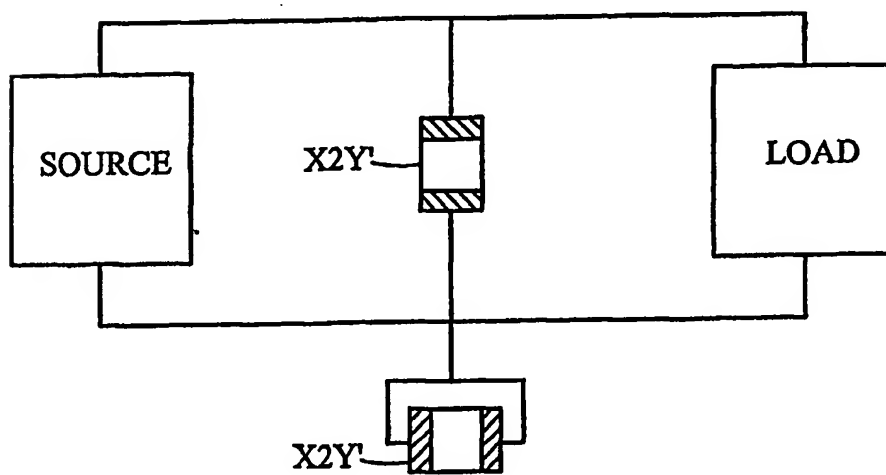


FIG. 22

18/22

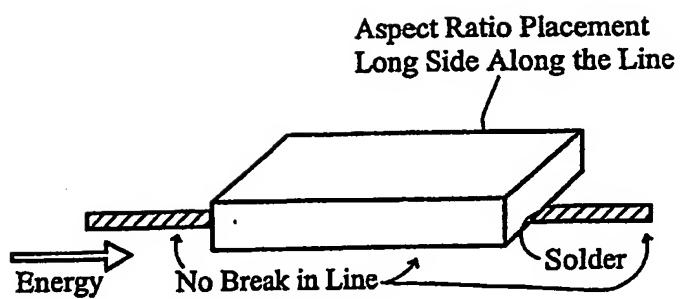


FIG. 23A

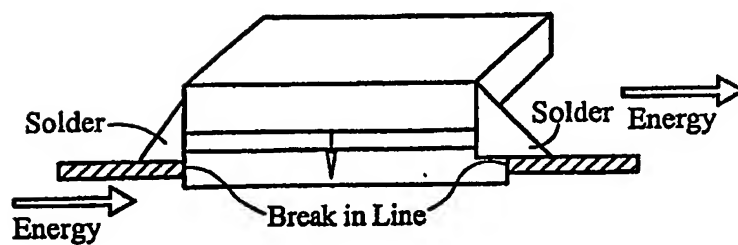


FIG. 23B

19/22

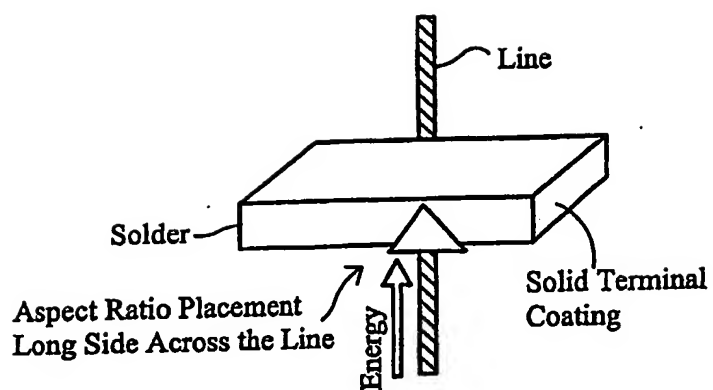


FIG. 23C

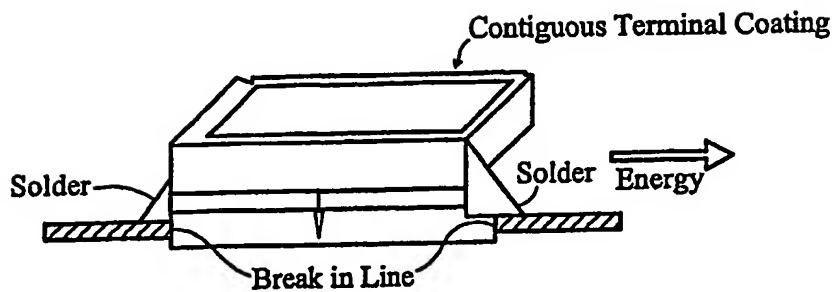
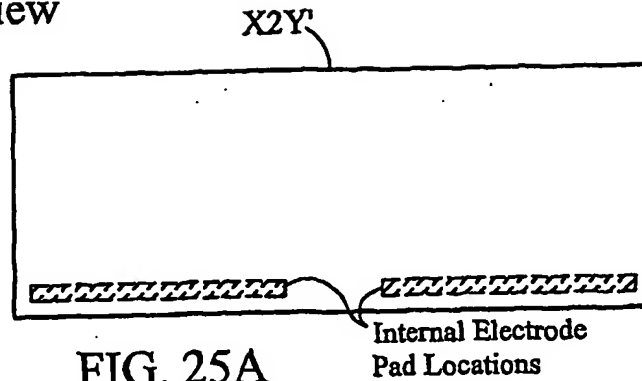


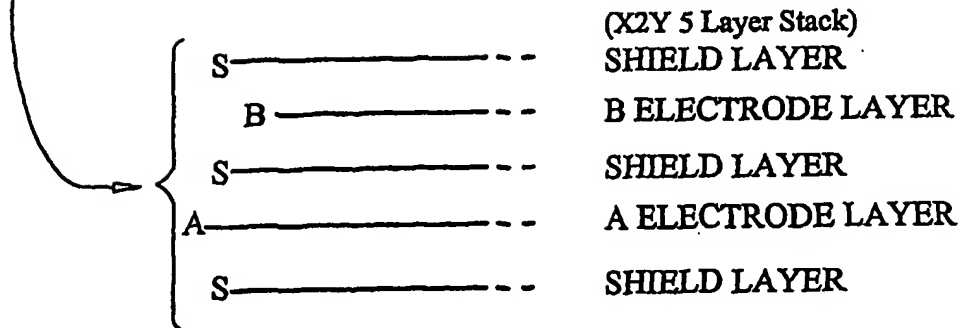
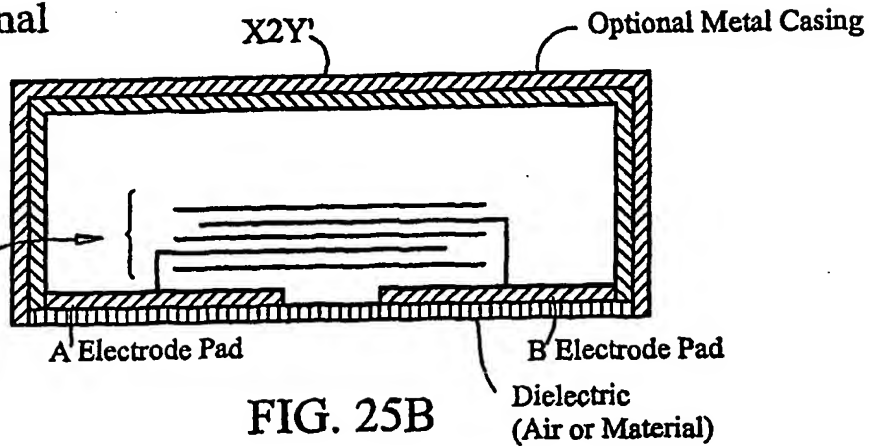
FIG. 24

20/22

Side View



Side
Sectional
View



21/22

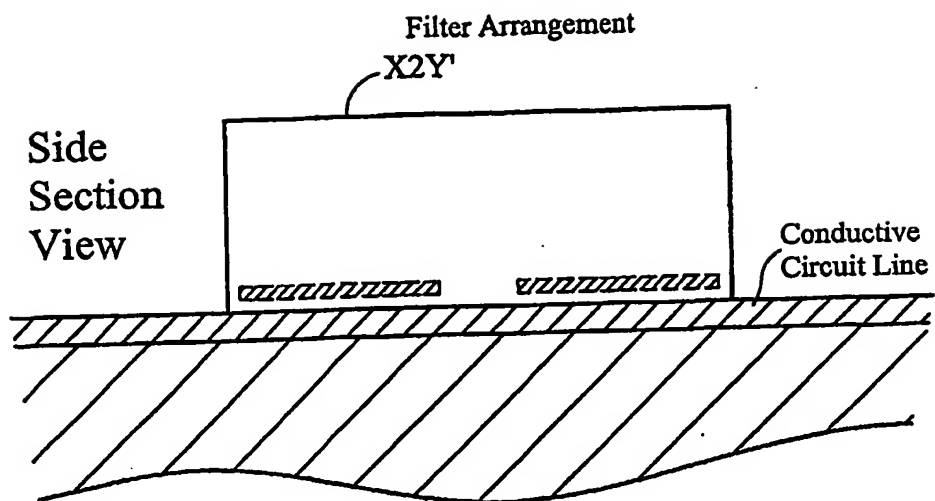


FIG. 26A

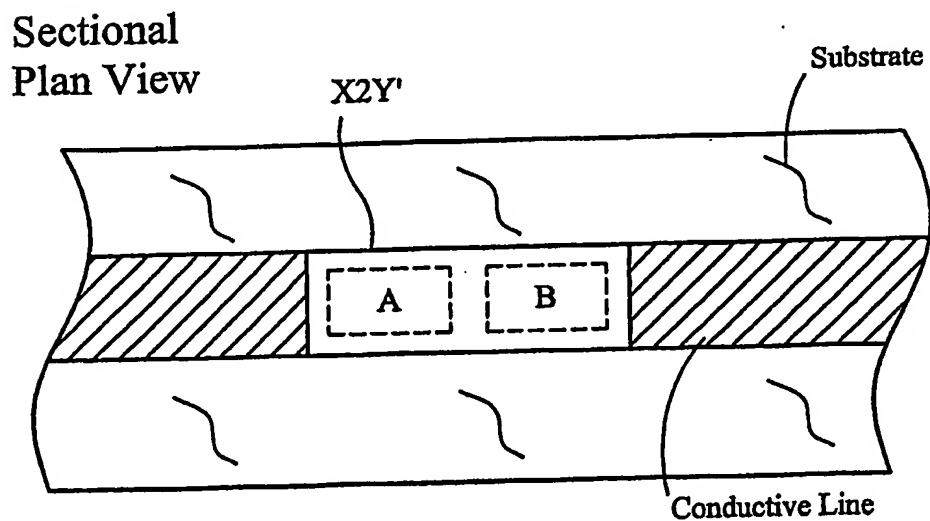


FIG. 26B

22/22

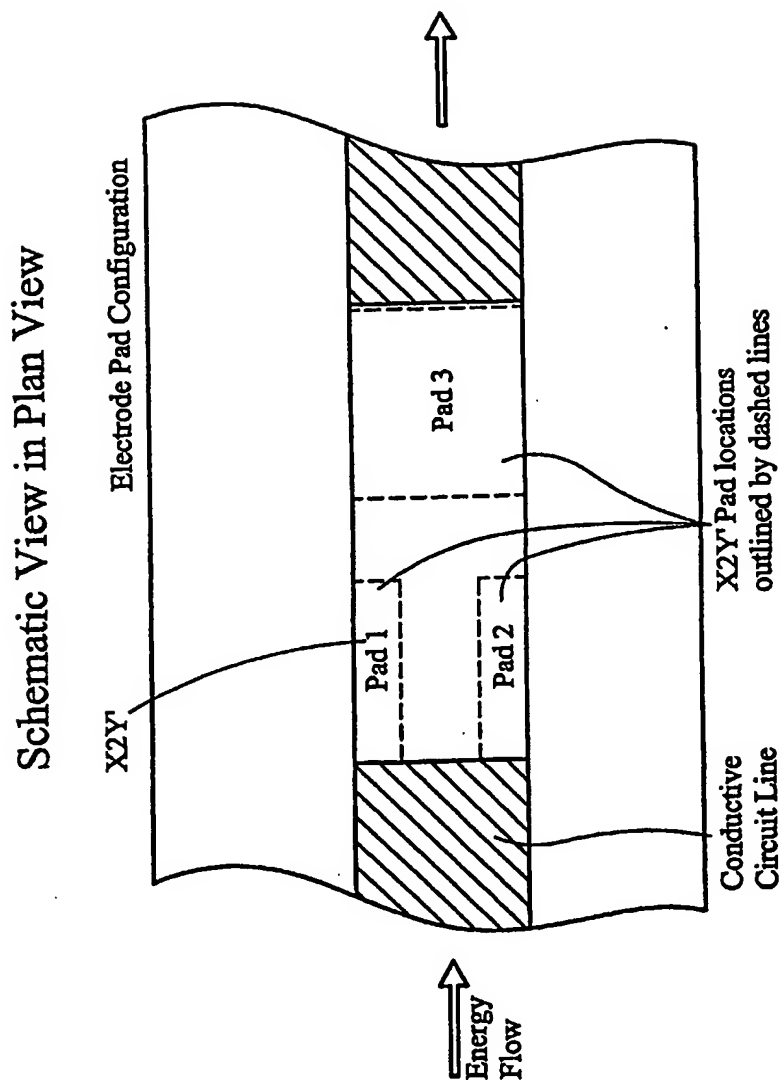


FIG. 27